

## 8-Output 3.3V PCIe Zero-Delay/Fanout Clock Buffer

Manufacturers

[Renesas Technology Corp](#)

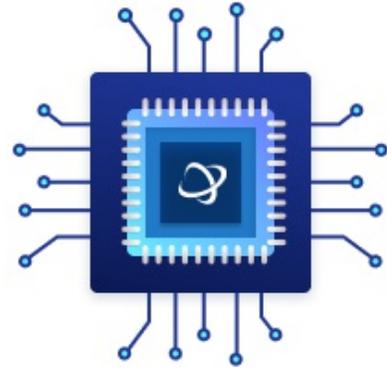
Package/Case

Product Type

Clock & Timer ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for 9DBL0841BKILF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The 9DBL0841 8-output zero-delay/fan-out buffer is a 3.3V member of Renesas' Full-Featured PCIe family. The 9DBL0841 supports PCIe Gen1 through Gen5 and both Common and Independent Reference Clock architectures.

For information regarding evaluation boards and material, please contact your local sales representative.

## Features

Additive PCIe Gen5 CC jitter < 60fs RMS (fan-out mode)

PCIe Gen5 CC jitter < 150fs RMS (High-BW ZDB Mode)

8 Low-Power HCSL (LP-HCSL) outputs eliminate 4 resistors per output pair

Direct connection to 100Ω transmission lines

Dedicated OE# pin for each output

Spread Spectrum tolerant

Pin or SMBus configuration

3 selectable SMBus addresses

SMBus interface not required for device operation

Easy AC-coupling to other logic families, see application note AN-891.

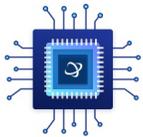
Space saving 48-pin 6 × 6 mm VFQFPN

## Related Products



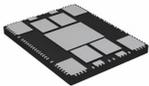
### [9DB106BFILE](#)

Renesas Technology Corp  
28-SSOP (0.209, 5.30mm Width)



### [9DML0451AKILF](#)

Renesas Technology Corp



### [9DBL0651BKILFT](#)

Renesas Technology Corp  
VFQFPN-40



### [9DBL0442BKILF](#)

Renesas Technology Corp



### [9DML0451AKILFT](#)

Renesas Technology Corp



### [9DML0441AKILF](#)

Renesas Technology Corp  
24-VFQFN



### [9DBL0651BKILF](#)

Renesas Technology Corp



### [9DBL0255NLGI8](#)

Renesas Technology Corp