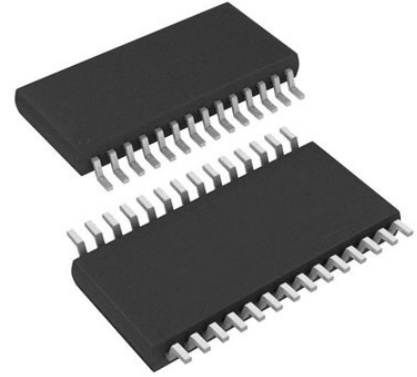


ZERO DELAY BUFF, 166MHZ, -40 TO 85DEG C New

Manufacturers	Renesas Technology Corp
Package/Case	28-TSSOP (0.173, 4.40mm Width)
Product Type	Clock & Timer ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 9DB433AGILF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 9DB433 zero-delay buffer supports PCIe Gen3 requirements, while being backwards compatible to PCIe Gen2 and Gen1. The 9DB433 is driven by a differential SRC output pair from an IDT 932S421 or 932SQ420 or equivalent main clock generator.

Features

Four 0.7V HCSL differential output pairs

Phase jitter: PCIe Gen3 < 1ps rms

Phase jitter: PCIe Gen2 < 3.1ps rms

Phase jitter: PCIe Gen1 < 86ps peak to peak

Supports zero delay buffer mode and fanout mode

Bandwidth programming available

33-110 MHz operation in PLL mode

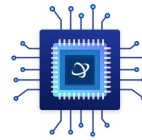
10-110 MHz operation in Bypass mode

Related Products



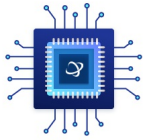
[9DB106BFILE](#)

Renesas Technology Corp
28-SSOP (0.209, 5.30mm Width)



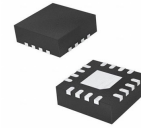
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Renesas Technology Corp



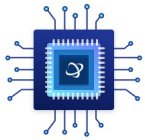
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Renesas Technology Corp



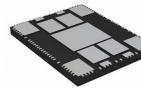
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Renesas Technology Corp
24-VFQFN



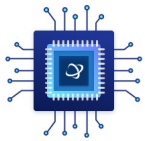
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Renesas Technology Corp



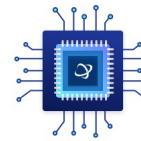
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Renesas Technology Corp
VFQFPN-40



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