

# AD7490BRUZ-REEL7

Data Sheet

Analog to Digital Converters - ADC 12-BIT 16CH IC w/ Sequencer

Manufacturers	Analog Devices, Inc	
Package/Case	TSSOP-28	Within the second
Product Type	Data Conversion ICs	1111
RoHS	Rohs	2222
Lifecycle		Images are for reference only

Please submit RFQ for AD7490BRUZ-REEL7 or Email to us; sales@oyaga.com We will contact you in 12 hours.

<u>RFQ</u>

## **General Description**

The conversion process and data acquisition are controlled using CS and the serial clock, allowing the device to interface with microprocessors or DSPs. The input signal is sampled on the falling edge of CS and conversion is also initiated at this point. There are no pipeline delays associated with the part.

The AD7490 uses advanced design techniques to achieve very low power dissipation at high throughput rates. For maximum throughput rates, the AD7490 consumes just 1.8 mA with 3 V supplies, and 2.5 mA with 5 V supplies. By setting the relevant bits in the control register, the analog input range for the part can be selected to be a 0 V to REFIN input or a 0 V to  $2 \times REFIN$  input, with either straight binary or twos complement output coding. The AD7490 features 16 single-ended analog inputs with a channel sequencer to allow a preprogrammed selection of channels to be converted sequen-tially. The conversion time is determined by the SCLK or twos complement output coding. The AD7490 features 16 single-ended analog inputs with a channel selection of channels to be converted sequen-tially. The conversion time is determined by the SCLK or twos complement output coding. The AD7490 features 16 single-ended analog inputs with a channel selection of channels to be converted sequen-tially. The conversion time is determined by the SCLK or two complement output coding. The AD7490 features 16 single-ended analog inputs with a channel selection of channels to be converted sequen-tially. The conversion time is determined by the SCLK or two complement output coding. The AD7490 features 16 single-ended analog inputs with a channel sequencer to allow a preprogrammed selection of channels to be converted sequen-tially. The conversion time is determined by the SCLK or two converted sequen-tially. The conversion time is determined by the SCLK or two converted sequen-tially. The conversion time is determined by the SCLK or two converted sequen-tially. The conversion time is determined by the SCLK or two converted sequen-tially.

The AD7490-EP supports defense and aerospace applications (AQEC)

### Features

Fast throughput rate: 1 MSPS Specified for VDD of 2.7 V-5.25 V Low power at maximum throughput rates 5.4 mW maximum at 870 kSPS with 3 V supplies 12.5 mW maximum at 1 MSPS with 5 V supplies 16 (single-ended) inputs with sequencer Wide input bandwidth 69.5 dB SNR at 50 kHz input frequency See data sheet for additional features AD7490-EP supports defense and aerospace applications (AQEC standard) Download(pdf) Military temperature range  $(-55^{\circ}C \text{ to } + 125^{\circ}C)$ Controlled manufacturing baseline One assembly/test site One fabrication site Enhanced product change notification Qualification data available on request

#### V62/12635 DSCC Drawing Number

#### **Related Products**



ADAS3022BCPZ

Analog Devices, Inc LFCSP-40







PDIP-28
AD7938BSUZ

Analog Devices, Inc TQFP-32



Multichannel system monitoring

Battery-powered equipment

Power line monitoring

Data acquisition, instrumentation, and process control







AD7266BSUZ

Analog Devices, Inc TQPF-32

#### AD7401YRWZ

Analog Devices, Inc SOIC-16

#### AD7192BRUZ-REEL

Analog Devices, Inc TSSOP-24



AD7124-8BCPZ-RL7

Analog Devices, Inc LFCSP-32



AD9680BCPZ-500

Analog Devices, Inc LFCSP-64