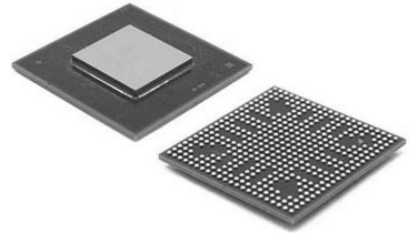


LDO Regulator Pos 0.5V to 1.5V 1A 16-Pin LFCSP EP T/R

Manufacturers	Analog Devices, Inc
Package/Case	LFCSP-16
Product Type	Power Management ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADP1761ACPZ-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADP1761 is a low noise, low dropout (LDO) linear regulator. It is designed to operate from a single input supply with an input voltage as low as 1.10 V, without the requirement of an external bias supply to increase efficiency and provide up to 1 A of output current.

The low 30 mV typical dropout voltage at a 1 A load allows the ADP1761 to operate with a small headroom while maintaining regulation and providing better efficiency. The ADP1761 is optimized for stable operation with small 10 μ F ceramic output capacitors.

The ADP1761 delivers optimal transient performance with minimal board area.

The ADP1761 is available in fixed output voltages ranging from 0.9 V to 1.5 V. The output of the adjustable output model can be set from 0.5 V to 1.5 V through an external resistor connected between VADJ and ground.

The ADP1761 has an externally programmable soft start time by connecting a capacitor to the SS pin. Short-circuit and thermal overload protection circuits prevent damage in adverse conditions. The ADP1761 is available in a small 16-lead LFCSP package for the smallest footprint solution to meet a variety of applications.

Features

1 A maximum output current

Low input voltage supply

Fixed output voltage range

Adjustable output voltage range

Application

Regulation to noise sensitive applications such as radio frequency (RF) transceivers, analog-to-digital converter (ADC) and digital-to-analog converter (DAC) circuits, phase-locked loops (PLLs), voltage controlled oscillators (VCOs) and clocking integrated circuits

Field-programmable gate array (FPGA) and digital signal processor (DSP) supplies

Medical and healthcare

Industrial and instrumentation

Ultra-low noise: 2 μ V rms,

100 Hz to 100 kHz

Noise spectral density

4 nV/ $\sqrt{\text{Hz}}$ at 10 kHz

3 nV/ $\sqrt{\text{Hz}}$ at 100 kHz

Low dropout voltage: 30
mV typical at 1 A load

Operating supply current:
4.5 mA typical at no load

Excellent power supply
rejection ratio (PSRR)
performance

67 dB typical at 10 kHz at
1 A load

51 dB typical at 100 kHz
at 1 A load

Excellent load/line
transient response

Soft start to reduce inrush
current

Optimized for small 10 μF
ceramic capacitors

Current-limit and thermal
overload protection

Power-good indicator

Precision enable

16-lead, 3 mm \times 3 mm
LFCSP package

Related Products



[ADP3336ARMZ-REEL7](#)

Analog Devices, Inc
MSOP-8



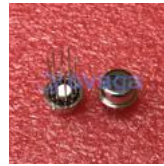
[AD737JRZ](#)

Analog Devices, Inc
SOP-8



[ADP3367ARZ](#)

Analog Devices, Inc
SOIC-8



[AD636JH](#)

Analog Devices, Inc
TO-100-10



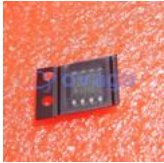
[ADP3330ARTZ3.3-RL7](#)

Analog Devices, Inc
SOT-23-6



[ADR434BRZ](#)

Analog Devices, Inc
SOIC-8



[ADR421ARZ](#)

Analog Devices, Inc
SOP-8



[ADR3412ARJZ-R7](#)

Analog Devices, Inc
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