

ADG1414BRUZ

Data Sheet

 $9.5~\Omega$ RON $\pm 15~V/\pm 12~V/\pm 5~V$ iCMOS Serially-Controlled Octal SPST Switches; Package: TSSOP 4.4 MM; No of Pins: 24; Temperature Range: Ind

Manufacturers <u>Analog Devices, Inc</u>

Package/Case TSSOP24

Product Type Interface - Switches, Multiplexers, Demultiplexers

RoHS Rohs

Lifecycle



Images are for reference only

Please submit RFQ for ADG1414BRUZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

The ADG1414 utilizes a versatile 3-wire serial interface that operates at clock rates of up to 50 MHz and is compatible with standard SPI®, QSPITM, MICROWIRETM, and DSP interface standards. The output of the shift register, SDO, enables a number of these parts to be daisy chained. On power-up, all switches are in the off condition, and the internal registers contain all zeros.

PRODUCT HIGHLIGHTS

50 MHz serial interface.

9.5 Ω on resistance. li>

 $1.6\,\Omega$ on-resistance flatness. li>

24-lead TSSOP and 4 mm × 4 mm LFCSP packages.

APPLICATIONS

Automatic test equipment

Data acquisition systems

Battery-powered systems

Sample-and-hold systems

Audio signal routing

Video signal routing

Communication systems

Features

SPI interface

Supports daisy-chain mode

 $10\,\Omega$ maximum on resistance

 $0.5~\Omega$ on-resistance flatness

Fully specified at ± 15 V, ± 12 V

3 V logic-compatible inputs

Rail-to-rail operation

24-lead TSSOP and 24-lead4 mm \times 4 mm LFCSP

Application

Automatic test equipment

Data acquisition systems

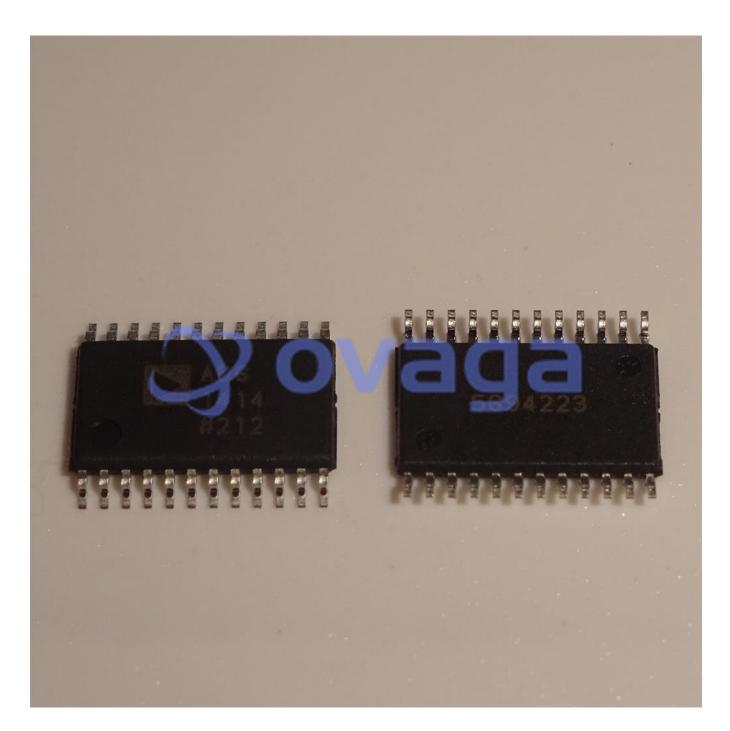
Battery-powered systems

Sample-and-hold systems

Audio signal routing

Video signal routing

Communication systems



Related Products



ADV7181CBSTZ

Analog Devices, Inc
LQFP-64



AD724JR
Analog Devices, Inc
SOIC-16



AD8170AR
Analog Devices, Inc
SOP8



ADV7393BCPZ
Analog Devices, Inc
LFCSP-VQ-40



ADV7391WBCPZ

Analog Devices, Inc LFSCP-3



ADV7390BCPZ

Analog Devices, Inc QFN32



ADV7341BSTZ
Analog Devices, Inc
LQFP-64



ADUM4160BRIZ

Analog Devices, Inc SOIC-16