

SRAM Chip Async Dual 3.3V 64K-Bit 4K x 16 20ns 100-Pin TQFP Tray

Manufacturers	Renesas Technology Corp
Package/Case	TQFP-100
Product Type	Memory
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 70V24L20PFGI or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

The 70V24 is a high-speed 4K x 16 Dual-Port Static RAM designed to be used as a stand-alone Dual-Port RAM or as a combination MASTER/SLAVE Dual-Port RAM for 32-bit or wider memory system applications resulting in full-speed, error-free operation without the need for additional discrete logic. An automatic power down feature controlled by CE permits the on-chip circuitry of each port to enter a very low standby power mode.

Features

True Dual-Ported memory cells which allow simultaneous reads of the same memory location

Separate upper-byte and lower-byte control for multiplexed bus compatibility

70V24 easily expands data bus width to 32 bits or more

BUSY and Interrupt Flag

On-chip port arbitration logic

Full on-chip hardware support of semaphore signaling between ports

Fully asynchronous operation from either port

LVTTL-compatible, single 3.3V ($\pm 0.3V$) power supply

Available in 100-pin TQFP and 84-pin PGA and PLCC packages

Industrial temperature range (-40C to +85C) is available for selected speeds

Green parts available, see ordering information

Related Products



[70V261L25PFGI](#)

Renesas Technology Corp
LQFP-100



[70V24L15PFG](#)

Renesas Technology Corp
100TQFP



[X28HC64SIZ-70](#)

Renesas Technology Corp
SOIC28



[71256S70DB](#)

Renesas Technology Corp
28-CDIP (0.600, 15.24mm)



[70V28L20PFGI](#)

Renesas Technology Corp
TQFP-100



[70V28L15PFG](#)

Renesas Technology Corp
TQFP-100



[70V27L15PFG](#)

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[70V261L25PFG](#)

Renesas Technology Corp
TQFP-100