

XC4VLX100-10FF1148C

Data Sheet

Virtex-4 FPGA	768 I/O	1148FCBGA
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Manufacturers	AMD Xilinx, Inc	€ XILINX® VIRTEX™4 XC4VLX100™ FF 1148DNQ1037	
Package/Case	BGA-1148	DD2606272A2	
Product Type	Programmable Logic ICs		
RoHS		Images are for reference only	
Lifecycle			
Please submit RFQ for XC4VLX100-10FF1148C or <u>Email to us: sales@ovaga.com</u> We will contact you in 12 <u>RFQ</u> hours.			

General Description

XC4VLX100-10FF1148C is a Field Programmable Gate Array (FPGA) device from the Xilinx Virtex-4 LX series, which was released in 2004.

Features	Application
The device has 100,160 logic cells and can support up to 416 I/O pins.	XC4VLX100- 11FF1148I
It operates at a maximum frequency of 500 MHz and has a total of 12.8 Mb of internal memory. The FPGA has a 10 ns minimum clock cycle time and uses a 1.2V core voltage.	XC4VLX100- 11FFG1148I
It also supports advanced features such as built-in high-speed serial transceivers, dynamic power management, and integrate RocketIO transceivers for high-speed serial communication.	ed XC4VLX100- 12FF1148C
	XC4VLX100- 12FF1148I
	XC4VLX100- 12FFG1148C
	XC4VLX100- 12FFG1148I
	XC4VLX100- 13FF1148C
	XC4VLX100- 13FF1148I
	XC4VLX100- 13FFG1148C
	XC4VLX100- 13FFG1148I.
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XC6SLX4-2CSG225C AMD Xilinx, Inc

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XC6SLX16-3CSG324C

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