

MOSFET & Power Driver ICs 3-PHASE FET

Manufacturers	<a href="#">Renesas Technology Corp</a>
Package/Case	SOIC-24
Product Type	Power Management ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for HIP4086AB or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The HIP4086 and HIP4086A (referred to as the HIP4086/A) are 3-phase N-channel MOSFET drivers. Both parts are specifically targeted for PWM motor control. These drivers have flexible input protocol for driving every possible switch combination. The user can even override the shoot-through protection for switched reluctance applications. The HIP4086/A have a wide range of programmable dead times (0.5 $\mu$ s to 4.5 $\mu$ s) which makes them very suitable for the low frequencies (up to 100kHz) typically used for motor drives. The only difference between the HIP4086 and the HIP4086A is that the HIP4086A has the built-in charge pumps disabled. This is useful in applications that require very quiet EMI performance (the charge pumps operate at 10MHz). The advantage of the HIP4086 is that the built-in charge pumps allow indefinitely long on times for the high-side drivers. To insure that the high-side driver boot capacitors are fully charged prior to turning on, a programmable bootstrap refresh pulse is activated when VDD is first applied. When active, the refresh pulse turns on all three of the low-side bridge FETs while holding off the three high-side bridge FETs to charge the high-side boot capacitors. After the refresh pulse clears, normal operation begins. Another useful feature of the HIP4086/A is the programmable undervoltage set point. The set point range varies from 6.6V to 8.5V.

## Features

Independently drives 6 N-channel MOSFETs in 3-phase bridge configuration

Bootstrap supply maximum voltage up to 95VDC with bias supply from 7V to 15V

1.25A peak turn-off current

User programmable dead time (0.5 $\mu$ s to 4.5 $\mu$ s)

Bootstrap and optional charge pump maintain the high-side driver bias voltage.

Programmable bootstrap refresh time

Drives 1000pF load with typical rise time of 20ns and fall time of 10ns

Programmable undervoltage set point





### Related Products



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SOIC-16



#### [HIP4080AIBZT](#)

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