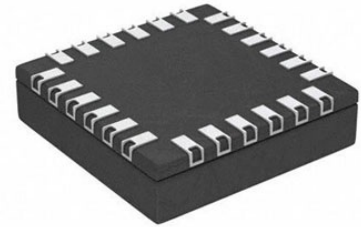


Analog Multiplexer Single 8:1 24-Pin LFCSP EP Tray

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	24-WFQFN, CSP
Product Type	Linear > Interface - Analog Switch Mux
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADGS1208BCPZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADGS1208/ADGS1209 are analog multiplexers comprising eight single channels and four differential channels, respectively. A serial peripheral interface (SPI) controls the switches. The SPI interface has robust error detection features, such as cyclic redundancy check (CRC) error detection, invalid read/write address detection, and SCLK count error detection.

It is possible to daisy-chain multiple ADGS1208/ADGS1209 devices together. Daisy-chain mode enables the configuration of multiple devices with a minimal amount of digital lines. The ADGS1208/ADGS1209 can also operate in burst mode to decrease the time between SPI commands.

iCMOS® construction ensures ultralow power dissipation, making the devices ideally suited for portable and battery-powered instruments.

Each switch conducts equally well in both directions when on, and each switch has an input signal range that extends to the supplies. In the off condition, signal levels up to the supplies are blocked.

The ultralow on capacitance (CON) and exceptionally low charge injection (QINJ) of these multiplexers make them ideal solutions for data acquisition and sample-and-hold applications, where low glitch and fast settling are required.

### Product Highlights

SPI interface removes the need for parallel conversion, logic traces and reduces GPIO channel count.

Daisy-chain mode removes additional logic traces when multiple devices are used.

CRC error detection, invalid read/write address detection, and SCLK count error detection ensures a robust digital interface.

CRC and Error detection capabilities allows for the use of the ADGS1208/ADGS1209 in safety critical systems.

## Features

SPI interface with error detection

Includes CRC, invalid read/write address, and SCLK count error detection

Supports burst mode and daisy-chain mode

Industry standard SPI Mode 0 and SPI Mode 3 interface compatible

Round robin mode allows switching times that are comparable with a parallel interface

Four general-purpose digital outputs that can be used to control other devices

1 pF off capacitance

VSS to VDD analog signal range

Fully specified at  $\pm 15$  V and +12 V

1.8 V logic compatibility with  $2.7 \text{ V} \leq V_L \leq 3.3 \text{ V}$

24-lead LFCSP package

## Application

Audio and video routing

Automatic test equipment

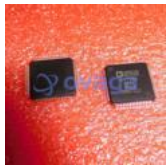
Data-acquisition systems

Battery-powered systems

Sample-and-hold systems

Communication systems

## Related Products



### [ADV7181CBSTZ](#)

Analog Devices, Inc  
LQFP-64



### [AD724JR](#)

Analog Devices, Inc  
SOIC-16



### [ADV7391WBCPZ](#)

Analog Devices, Inc  
LFSCP-3



### [ADV7341BSTZ](#)

Analog Devices, Inc  
LQFP-64



### [AD8170AR](#)

Analog Devices, Inc  
SOP8



### [ADV7393BCPZ](#)

Analog Devices, Inc  
LFCSP-VQ-40



### [ADV7390BCPZ](#)

Analog Devices, Inc  
QFN32



### [ADUM4160BRIZ](#)

Analog Devices, Inc  
SOIC-16