

DSP Fixed-Point 16bit 66MHz 66MIPS 100-Pin LQFP Tray

Manufacturers	Analog Devices, Inc
Package/Case	LQFP100
Product Type	Embedded Processors & Controllers
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADSP-2189MBSTZ-266 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADSP-2189M is notable for its very large on-chip memory (1.5 MBits) and low power features (2.5v).

The ADSP-2189M combines the ADSP-2100 family base architecture (three computational units, data address generators and a program sequencer) with two serial ports, a 16-bit internal DMA port, a byte DMA port, a programmable timer, Flag I/O, and extensive interrupt capabilities.

Features

Algebraic assembly language supports C language-style constructs for easy programming

100-lead LQFP package provides for space and power savings (16mm x 16mm)

On-chip RAM and 4 DMA ports make reprogramming easy

16-bit DMA port makes bus interfacing easier

5 sleep and powerdown modes maximize battery life.

13 ns instruction cycle time (75 MIPS)

2.5 volt supply with 3.3 volt I/O

32K words of on-chip program RAM

48K words of on-chip data memory RAM

16-bit internal DMA port for high speed access to on-chip memory

8-bit DMA port to byte memory for transparent program and data memory transfers

Application

0.4 mA/MIPS

Byte memory interface for storage of data tables and program overlays

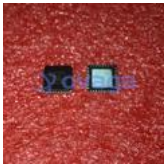
Programmable memory strobe and separate I/O memory space permits "glueless" system

ADSP-2100 Family code and function compatible

100-lead LQFP

144 mini-BGA package

Related Products



[ADUC7022BCPZ62](#)

Analog Devices, Inc
LFCSP-40



[ADUC841BSZ62-5](#)

Analog Devices, Inc
QFP-52



[ADUC831BSZ](#)

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QFP-52



[ADSP-21369BBPZ-2A](#)

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SBGA-256



[ADUC7020BCPZ62](#)

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LFCSP-40



[ADUC841BSZ62-3](#)

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QFP-52



[ADSP-BF527BBCZ-5A](#)

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BGA-208



[ADSP-BF561SBBCZ-5A](#)

Analog Devices, Inc
CSPBGA-256