

Pb-Free w/Anneal PERIPH CLK GENERATOR 5V 25MHZ 18PDIP COM HT SUSA
 CODE:8542390000

Manufacturers	Renesas Technology Corp
Package/Case	PDIP-18
Product Type	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for CP82C84AZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The Intersil 82C84A is a high performance CMOS Clock Generator/driver which is designed to service the requirements of both CMOS and NMOS microprocessors such as the 80C86, 80C88, 8086 and the 8088. The chip contains a crystal controlled oscillator, a divide-by-three counter and complete "Ready" synchronization and reset logic. Static CMOS circuit design permits operation with an external frequency source from DC to 25MHz. Crystal controlled operation to 25MHz is guaranteed with the use of a parallel, fundamental mode crystal and two small load capacitors. All inputs (except X1 and RES) are TTL compatible over temperature and voltage ranges. Power consumption is a fraction of that of the equivalent bipolar circuits. This speed-power characteristic of CMOS permits the designer to custom tailor his system design with respect to power and/or speed requirements.

Features

Generates the System Clock For CMOS or NMOS Microprocessors

Up to 25MHz Operation

Uses a Parallel Mode Crystal Circuit or External Frequency Source

Provides Ready Synchronization

Generates System Reset Output From Schmitt Trigger Input

TTL Compatible Inputs/Outputs

Very Low Power Consumption

Single 5V Power Supply

Operating Temperature Ranges

C82C84A 0°C to +70°C

I82C84A -40°C to +85°C

M82C84A -55°C to +125°C

Pb-Free Plus Anneal Available (RoHS Compliant)

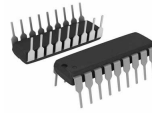


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