

Supervisory Circuits CPU SUP/WDT 32K SPI EE RST LW 2 7-5 5V

Manufacturers	Renesas Technology Corp
Package/Case	SOP-8
Product Type	Power Management ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for X5323S8Z-2.7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

These devices combine four popular functions, Power-on Reset Control, Watchdog Timer, Supply Voltage Supervision, and Block Lock Protect Serial EEPROM Memory in one package. This combination lowers system cost, reduces board space requirements, and increases reliability. Applying power to the device activates the power-on reset circuit which holds RESET/RESET active for a period of time. This allows the power supply and oscillator to stabilize before the processor can execute code. The Watchdog Timer provides an independent protection mechanism for microcontrollers. When the microcontroller fails to restart a timer within a selectable time out interval, the device activates the RESET/RESET signal. The user selects the interval from three preset values. Once selected, the interval does not change, even after cycling the power. The Device's low VCC detection circuitry protects the user's system from low voltage conditions, resetting the system when VCC falls below the minimum VCC trip point. RESET/RESET is asserted until VCC returns to proper operating level and stabilizes. Five industry standard TRIP thresholds are available, however, Intersil's unique circuits allow the threshold to be reprogrammed to meet custom requirements or to fine-tune the threshold for applications requiring higher precision.

Features

Selectable watchdog timer

Low V

CC

Five standard reset threshold voltages

Re-program low V

CC

Reset signal valid to>

Determine watchdog or low voltage reset with a volatile flag bit

Long battery life with low power consumption

32Kbits of EEPROM

Built-in inadvertent write protection

Power-up/power-down protection circuitry

Protect 0, 1/4, 1/2 or all of EEPROM array with Block Lock™ protection

In circuit programmable ROM mode

2MHz SPI interface modes (0,0 & 1,1)

Minimize EEPROM programming time

32-byte page write mode

Self-timed write cycle

5ms write cycle time (typical)

2.7V to 5.5V and 4.5V to 5.5V power supply operation

Available packages

14 Ld TSSOP, 8 Ld SOIC, 8 Ld PDIP

Pb-free plus anneal available (RoHS compliant)



Related Products



[X5323S8IZT1](#)

Renesas Technology Corp
SOP8



[X5043S8IZT1](#)

Renesas Technology Corp
SOP-8



[X5043PIZ](#)

Renesas Technology Corp
PDIP-8



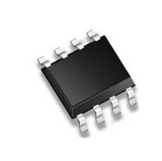
[X5325S8ZT1](#)

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[X5325S8Z-4.5A](#)

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[X5325S8Z-2.7T1](#)

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8-SOIC



[X5325S8Z-2.7A](#)

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[X5325S8Z-2.7](#)

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8-SOIC (0.154, 3.90mm Width)