

XCS20-3PQG208I

Data Sheet

XCS20-3PQG208I - NOT RECOMMENDED for NEW DESIGN

Manufacturers AMD Xilinx, Inc

Package/Case QFP-208

Product Type Programmable Logic ICs

Lifecycle

RoHS



Images are for reference only

Please submit RFQ for XCS20-3PQG208I or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFQ

General Description

XCS20-3PQG208I is a field-programmable gate array (FPGA) manufactured by Xilinx.

Features

It has 20,160 logic cells and 504 kilobits of block RAM.

It operates at a maximum clock frequency of 450 MHz.

It has 89 input/output (I/O) pins and supports various I/O standards such as LVCMOS, LVTTL, LVDS, and differential HSTL.

Application

XCS20-3PQG208I can be used in a wide range of applications such as digital signal processing, wireless communications, video processing, and networking.

It is commonly used in industrial control, automotive, aerospace, and defense applications.





Related Products



XC18V01S020C

AMD Xilinx, Inc SOP-20



XCF04SV0G20C

AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C

AMD Xilinx, Inc BGA-225



XCV50-6BG256C

AMD Xilinx, Inc BGA256



XCF08PV0G48C

AMD Xilinx, Inc TSOP-48



XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256



XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324



XCF32PVO48C

AMD Xilinx, Inc TSOP48