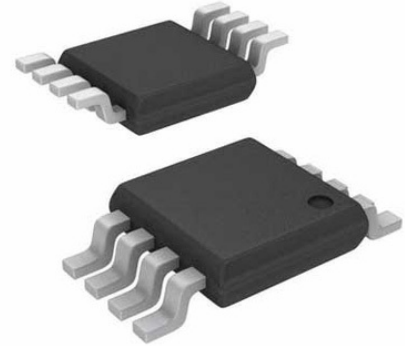


EEPROM, 512 Kbit, 64K x 8bit, Serial I2C (2-Wire), 1 MHz, TSSOP, 8 Pins

Manufacturers	Microchip Technology, Inc
Package/Case	TSSOP-8
Product Type	Memory
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AT24C512C-XHD-B or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The CAT24C512C8UTR is a 512 Kb Serial CMOS EEPROM, internally organized as 65,536 words of 8 bits each. It features a 128-byte page write buffer and supports the Standard (100 kHz), Fast (400 kHz) and Fast-Plus (1 MHz) I2C protocol. Write operations can be inhibited by taking the WP pin High (this protects the entire memory). External address pins make it possible to address up to eight CAT24C512C8UTR devices on the same bus. On-Chip ECC (Error Correction Code) makes the device suitable for high reliability applications.

Features

- Supports Standard, Fast and Fast-Plus I2C Protocol
- 1.8 V to 5.5 V Supply Voltage Range
- 128-Byte Page Write Buffer
- Hardware Write Protection for Entire Memory
- Schmitt Triggers and Noise Suppression Filters on I2C Bus Inputs(SCL and SDA)
- Low Power CMOS Technology
- 1,000,000 Program/Erase Cycles
- 100 Year Data Retention
- Industrial and Extended Temperature Range
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Application

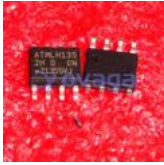
- Wireless devices - Cell phones (Smartphone)
- Smartphone
- Wireless devices

Related Products



[AT27C010-45JU](#)

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PLCC-32



[AT24CM02-SSHD-B](#)

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SOIC-8



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[AT24C04D-MAHM-T](#)

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[AT28C010E-12DM/883](#)

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