

85000 GATE 3.3 VOLT LOGIC CELL ARRAY - NOT RECOMMENDED for NEW DESIGN FPGA

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP-240
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC4085XLA-08HQ240I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC4085XLA-08HQ240I is a complex programmable logic device (CPLD) from the Xilinx XC4000XLA family. Here are some of its features:

Features

- It has 85,000 system gates.
- It has 192 macrocells (configurable logic blocks).
- It has a maximum pin count of 240.
- It has a maximum user I/O count of 193.
- It operates on a 3.3V power supply.
- It has a high-speed global clock network.

Application

- Control and data path management in telecommunications systems.
- High-performance logic replacement in ASIC-based designs.
- Digital signal processing (DSP) functions such as filtering and modulation.
- High-speed control logic in embedded systems.



Related Products



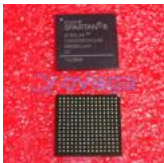
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



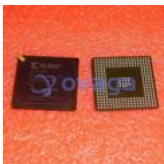
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[XC6SLX4-2CSG225C](#)

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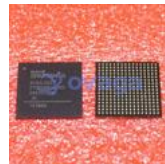
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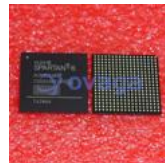
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