

5PB1106PGGI

Data Sheet

CLOCK BUFFER, 200MHZ, -40 TO 85DEG C New

Manufacturers Renesas Technology Corp

Package/Case TSSOP-14

Product Type Integrated Circuits (ICs)

RoHS

Lifecycle Images are for reference only

Please submit RFQ for 5PB1106PGGI or Email to us: sales@ovaga.com We will contact you in 12 hours.



General Description

The 5PB1106 is a high-performance 1:6 LVCMOS clock buffer. It has best-in-class Additive Phase Jitter of 50 fsec RMS.

The 5PB1106 also supports an Output Enable function. It is available in 16-pin QFN and 14-pin TSSOP packages and can operate from a 1.8 V to 3.3 V supply.

Features

High performance 1:6 LVCMOS clock buffer

Very low pin-to-pin skew <50 ps

Very low additive jitter <50 fs

Supply voltage: 1.8 V to 3.3 V

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Integrated serial termination for 50ohm channel

Packaged in 14-pin TSSOP and small 16-pin QFN packages

Extended (-40°C to +105°C) temperature range

Related Products



HD74HC125P

Renesas Technology Corp DIP-14



5PB1106CMGI

Renesas Technology Corp COL-16



5PB1102CMGI8

Renesas Technology Corp COL-8



R5F64175PFB#UC

Renesas Technology Corp



5PB1213NTGK

Renesas Technology Corp VFQFPN-10



5PB1102PGGI8

Renesas Technology Corp TSSOP-8



5PB1102CMGI

Renesas Technology Corp 8-COL



R5S72645P144FPU

Renesas Technology Corp QFP-208