

FPGA Spartan®-3A Family 700K Gates 13248 Cells 667MHz 90nm Technology 1.2V

Manufacturers	AMD Xilinx, Inc
Package/Case	484FBGA
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC3S700A-4FGG484I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC3S700A-4FGG484I is a Field-Programmable Gate Array (FPGA) from Xilinx, which is a programmable integrated circuit that allows for the customization of digital circuits after manufacturing.

Features

- It has 700K system gates, which represents the number of logic cells or functional blocks in the FPGA.
- It has 92,160 logic cells, 2,520Kbits of block RAM, and 228 DSP48A1 slices.
- It has a 4-input look-up table (LUT) architecture, which allows for flexible logic implementation.
- It supports up to 500MHz clock frequency.
- It has various I/O standards, including LVCMOS, LVTTTL, HSTL, SSTL, and LVDS.
- It operates on a 1.2V core voltage and can interface with 1.5V to 3.3V I/O standards.
- It is housed in a Fine-Pitch Ball Grid Array (FBGA) package.

Application

- Digital signal processing
- High-speed networking
- Video and image processing
- Aerospace and defense systems
- Industrial control and automation
- Test and measurement equipment

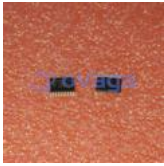


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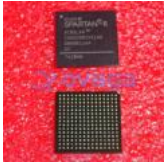
[XC18V01S020C](#)

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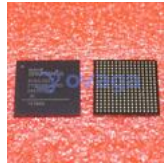
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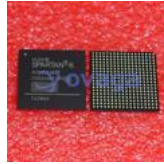
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