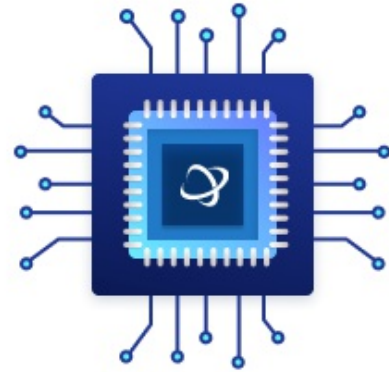


553S MIXED SIGNAL; HT SUSA CODE:8542390000

Manufacturers	Renesas Technology Corp
Package/Case	COL-8
Product Type	Clock & Timer ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 553SCMG18 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 553S is a low skew, single input to four output, LVCMOS clock buffer. The 553S has best in class additive phase Jitter of sub 50 fsec.

Features

Low additive phase jitter RMS: 50fs

Extremely low skew outputs (50ps)

Low cost clock buffer

Packaged in 8-pin SOIC and small 8-pin DFN packages, Pb-free

Input / Output clock frequency up to 200MHz

Ideal for networking clocks

Operating voltages: 1.8V to 3.3V

Output Enable mode tri-states outputs

Advanced, low power CMOS process

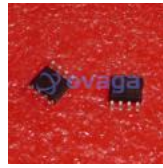
Extended temperature range (-40°C to +105°C)

Related Products



[ICM7555IBAZ-T](#)

Renesas Technology Corp
SOIC-8



[557MI-01LF](#)

Renesas Technology Corp
SOIC-8



[553MILFT](#)

Renesas Technology Corp
SOIC-8



[553MILF](#)

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DFN8



[552G-02ILN](#)

Renesas Technology Corp
TSSOP-16