

9DBL0651B 3.3V LP-HCSL PCIE ZDB/FOB

Manufacturers

[Renesas Technology Corp](#)

Package/Case

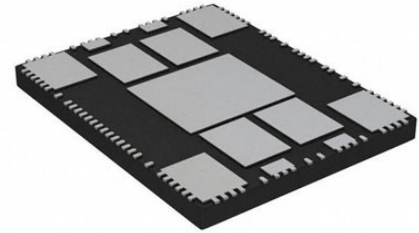
VFQFPN-40

Product Type

Clock & Timer ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for 9DBL0651BKILFT or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

The 9DBL0651 device is a 3.3V member of IDT's Full-Featured PCIe family. The 9DBL0651 supports PCIe Gen1-4 Common Clocked (CC) and PCIe Separate Reference Independent Spread (SRIS) systems. It offers an integrated output termination providing direct connection to 85Ω transmission lines. The 9DBL06P1 can be factory programmed with a user-defined power up default SMBus configuration.

Features

Additive PCIe Gen5 CC jitter < 60fs RMS (fan-out mode)

PCIe Gen5 CC jitter < 150fs RMS (High-BW ZDB Mode)

6 Low-Power HCSL (LP-HCSL) outputs eliminate 4 resistors per output pair

Direct connection to 85Ω transmission lines

Dedicated OE# pin for each output

Spread Spectrum tolerant

Pin or SMBus configuration

3 selectable SMBus addresses

SMBus interface not required for device operation

Easy AC-coupling to other logic families, see application note AN-891.

Space saving 40-pin 5 × 5mm VFQFPN

Related Products



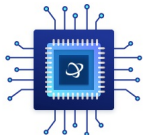
[9DB106BFILF](#)

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28-SSOP (0.209, 5.30mm Width)



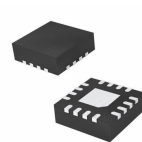
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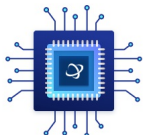
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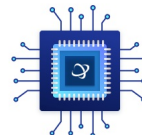
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24-VFQFN



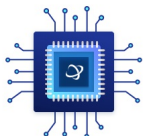
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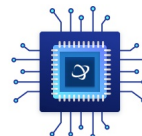
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