

SST25VF040B-50-4I-SAF

Data Sheet

Flash Memory, Serial NOR, 4 Mbit, 512K x 8bit, SPI, SOIC, 8 Pins

| Manufacturers | Microchip Technology, Inc | C. F. F. F. |
|---------------|---------------------------|-------------------------------|
| Package/Case | SOIC-8 | |
| Product Type | Memory | EEEE |
| RoHS | Rohs | |
| Lifecycle | | Images are for reference only |
| | | |

Please submit RFQ for SST25VF040B-50-4I-SAF or Email to us: sales@oyaga.com We will contact you in 12 hours.

<u>RFQ</u>

General Description

The 25 series Serial Flash family features a four-wire, SPIcompatible interface that allows for a low pin-count package which occupies less board space and ultimately lowers total system costs. The SST25VF040B devices are enhanced with improved operating frequency for lower power consumption. SST25VF040B SPI serial flash memories are manufactured with SST proprietary, high-performance CMOS SuperFlash technology. The split-gate cell design and thick-oxide tunneling injector attain better reliability and manufacturability compared with alternate approaches.

Please consider this deviceSST25PF040C

Features

SPI Compatible: Mode 0 and Mode 3

Supports 50 MHz SPI clock (80MHz no longer available see EOL NOTIFICATION)

Active Read Current: 10 mA (typical)

Program & Erase Current: 30mA (max)

Standby Current: 5 µA (typical)

Uniform 4 KByte sectors

Uniform 32 KByte and 64 KByte overlay blocks

Chip-Erase Time: 35 ms (typical)

Sector-/Block-Erase Time: 18 ms (typical)

Byte-Program Time: 7 µs (typical)

Decrease total chip programming time overByte-Program operations

8-lead SOIJ (200 mils)

8-lead SOIC (150 mils)

8-contact WSON (6mm x 5mm)

Chip Scale Package

Related Products



AT24CM02-SSHM-B Microchip Technology, Inc





SOIC-8







Microchip Technology, Inc TSOP-48







SST39VF3201-70-4C-B3KE Microchip Technology, Inc

TFBGA-48

AT24CM02-SSHD-B

SOIC-8

Microchip Technology, Inc



SST39VF1601C-70-4I-EKE

Microchip Technology, Inc

TSOP-48



AT24CM01-SSHM-T

Microchip Technology, Inc SOIC-8