

XCR3256XL-7TQG144C

Data Sheet

CPLD CoolRunner XPLA3 Family 6K Gates 256 Macro Cells 154MHz 0.35um (CMOS) Technology 3.3V

Manufacturers <u>AMD Xilinx, Inc</u>

Package/Case 144-LQFP

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for XCR3256XL-7TQG144C or <u>Emailto:sales@ovaga.com</u> We will contact you in 12 hours.



General Description

XCR3256XL-7TQG144C is an FPGA (Field Programmable Gate Array) from the Xilinx CoolRunnerTM-II CPLD family.

Features

It has a total of 256 macrocells (logic blocks) that can be programmed to implement any digital logic function.

It offers a maximum of 312 user I/O pins.

It supports a wide range of interface standards including LVCMOS, LVTTL, PCI, and others.

It operates on a 1.8V core voltage and supports 3.3V and 2.5V I/O standards.

It has a maximum operating frequency of 333MHz and can achieve up to 1.2 million system gates.

Application

It is commonly used in communication systems, such as routers, switches, and other network devices.

It is used in video processing and display applications.

It is also used in control and monitoring systems, including industrial automation and robotics.

It is used in medical equipment, military and aerospace applications, and automotive electronics.



Related Products



XC18V01S020C

AMD Xilinx, Inc SOP-20



XCF04SV0G20C

AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C

AMD Xilinx, Inc BGA-225



XCV50-6BG256C

AMD Xilinx, Inc BGA256



XCF08PV0G48C

AMD Xilinx, Inc TSOP-48



XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256



XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324



XCF32PVO48C

AMD Xilinx, Inc TSOP48