

Flip-Flop, CML, Positive Edge, D, 400 ps, 2.5 GHz, MSOP

Manufacturers	Microchip Technology, Inc
Package/Case	MSOP-10
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for SY55852UKG or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The SY55852U is a flip-flop used to synchronize data to a clock. Its differential output will reproduce and remember the value on its input at the rising edge of the clock. In addition, an asynchronous, level sensitive reset is provided. For a synchronous reset, the SY55851U AnyGate® can be used. SY55852U inputs can be terminated with a single resistor between the true and complement pins of a given input. The SY55852U is a member of Micrel's SuperLite™ family of high-speed CML logic. This family features very small packaging and 2.3V to 5.7V operation.

Features

2.5GHz min. fMAX

2.3V to 5.7V power supply

Single bit register memory

Synchronizes 1 bit of data to a clock

Optimized to work with SuperLite™ family

Fully differential

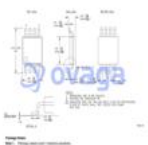
Accepts CML, PECL, LVPECL input logic levels

Source terminated CML outputs for fast edge rates

Available in a tiny 10-pin MSOP

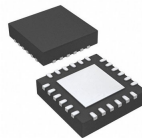


Related Products



[SY100EL16VKG](#)

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MSOP-8



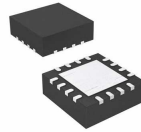
[SY89474UMG](#)

Microchip Technology, Inc
VQFN-24



[SY100ELT23ZG](#)

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SOIC-8



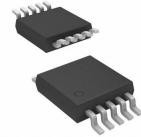
[SY58051AUMG-TR](#)

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VQFN-16



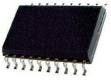
[SY55852UKG-TR](#)

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10-TFSOP, 10-MSOP (0.118, 3.00mm Width)



[SY55851AUKG-TR](#)

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10-TFSOP, 10-MSOP (0.118, 3.00mm Width)



[SY100EL29VZG-TR](#)

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