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ATWINC3400-MR210CA131-T

Data Sheet

RFO

SmartConnect Module

Manufacturers	Microchip Technology, Inc	
Package/Case	SMD	
Product Type		
RoHS		
Lifecycle		Images are for reference only

Please submit RFQ for ATWINC3400-MR210CA131-T or Email to us: sales@oyaga.com We will contact you in 12 hours.

General Description

Microchip's ATWINC3400-IC is an IEEE 802.11 b/g/n plus Bluetooth 5 Core Specification, Internet of Things (IoT) network controller IC. It is the ideal add-on to existing microcontroller (MCU) solutions, offering Wi-Fi® and Bluetooth® Low Energy (BLE) network capabilities through a SPI-to-Wi-Fi interface. The ATWINC3400 connects to any Microchip PIC® or SAM MCU with minimal resource requirements.

A MCU host driver can be found in the Advanced Software Framework (ASF) and in MPLAB(r) Harmony v3.

The ATWINC3400's most advanced mode is a single-stream 1 x 1 802.11n mode. The ATWINC3400 features a fully integrated power amplifier, LNA, switch and power management. The ATWINC3400 also features an on-chip processor and integrated Flash memory for system software with built-in coexistence mechanism for concurrent Wi-Fi and BLE functionality. The only external clock source needed for the ATWINC3400 is a high-speed crystal or oscillator. The ATWINC3400 is available in a 48-pin VQFN package with dimensions of 6x6mm.

Microchip's complimentary and confidential Wireless Check online design review service is available for customers who have selected our products for their application design-in*.

*The online design review service is subject to Microchip's Program Terms and Conditions and requires a myMicrochip account.

Click here for our Firmware Release Summary

Available in a fully-certified modules with u.FL connector or PCB antenna.

Features

IEEE 802.11 b/g/n 20MHz (1x1) solution

Single spatial stream in 2.4GHz ISM band

Integrated PA and T/R Switch

Superior Sensitivity and Range via advanced PHY signal processing

Operating temperature range of -40C to +85C

On-Chip Network Stack to offload MCU:Integrated Network IP stack to minimize host CPU requirementsNetwork features: TCP, UDP, DHCP, ARP, HTTP, SSL, and DNS

Integrated Network IP stack to minimize host CPU requirements

Network features: TCP, UDP, DHCP, ARP, HTTP, SSL, and DNS

On-Chip Network Stack to offload MCU

BLE 5.0

Class 2 transmission and Adaptive Frequency Hopping

Integrated PA and T/R Switch

Superior Sensitivity and Range

Related Products



ATA6563-GAQW0

Microchip Technology, Inc SOIC-8





Microchip Technology, Inc WDFN-8







AT42QT1040-MMHR Microchip Technology, Inc

AT30TS74-SS8M-T

VQFN-20

Microchip Technology, Inc SOIC-8

ATSAMC21G17A-MZTVAO

Microchip Technology, Inc VQFN

ATMEGA808-MU

Microchip Technology, Inc





ATTINY3226-SU

Microchip Technology, Inc

SOIC



ATTINY3224-SSU

Microchip Technology, Inc SOIC