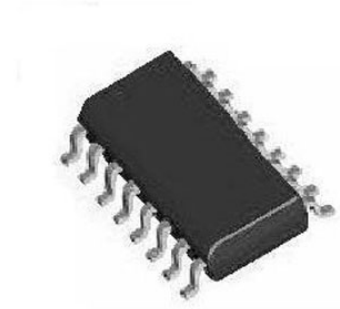


Slew-Rate Limited Isolated RS-485 Transceiver; Package: SOIC - Wide; No of Pins: 16;
Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	SOIC-16
Product Type	Interface ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADM2483BRWZ-REEL or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADM2483 differential bus transceiver is an integrated, galvanically isolated component designed for bidirectional data communication on balanced, multipoint bus transmission lines. It complies with ANSI EIA/TIA-485-A and ISO 8482: 1987(E). Using the iCoupler technology from Analog Devices, Inc., the ADM2483 combines a 3-channel isolator, a three-state differential line driver, and a differential input receiver into a single package. The logic side of the device is powered with either a 5 V or 3 V supply, and the bus side uses a 5 V supply only.

The ADM2483 is slew-limited to reduce reflections within properly terminated transmission lines. The controlled slew rate limits the data rate to 500 kbps. The device's input impedance is 96 kΩ, allowing up to 256 transceivers on the bus. Its driver has an active-high enable feature. The driver differential outputs and receiver differential inputs are connected internally to form a differential input/output (I/O) port.

When the driver is disabled or when VDD1 or >

Current limiting and thermal shutdown features protect against output short circuits and bus contention situations that might cause excessive power dissipation. The part is fully specified over the industrial temperature range and is available in a 16-lead, wide body SOIC package.

Features

RS-485 transceiver with electrical data isolation

Complies with ANSI TIA/EIA RS-485-A and ISO 8482: 1987(E)

500 kbps data rate

Slew rate-limited driver outputs

Low power operation: 2.5 mA max

Suitable for 5 V or 3 V operations (VDD1)

High common-mode transient immunity: >25 kV/ μ s

True fail-safe receiver inputs

Chatter-free power-up/power-down protection

256 nodes on bus

Thermal shutdown protection

Safety and regulatory approvals

UL recognition: 2500 V rms for 1 minute per UL 1577

CSA Component Acceptance Notice 5A

IEC 60950-1 800 V rms (basic), 400V rms (reinforced)

VDE Certificate of Conformity

DIN V VDE V 0884-10 (VDE V 0884-10)>

CQC certification per GB4943.1-2011

Operating temperature range: -40°C to +85°C

Application

Low power RS-485/RS-422 networks

Isolated interfaces

Building control networks

Multipoint data transmission systems

Related Products



[ADV7181CBSTZ](#)

Analog Devices, Inc
LQFP-64



[AD8170AR](#)

Analog Devices, Inc
SOP8



[AD724JR](#)

Analog Devices, Inc
SOIC-16



[ADV7393BCPZ](#)

Analog Devices, Inc
LFCSP-VQ-40



[ADV7391WBCPZ](#)

Analog Devices, Inc
LFSCP-3



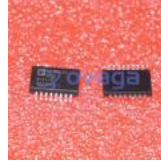
[ADV7390BCPZ](#)

Analog Devices, Inc
QFN32



[ADV7341BSTZ](#)

Analog Devices, Inc
LQFP-64



[ADUM4160BRIZ](#)

Analog Devices, Inc
SOIC-16