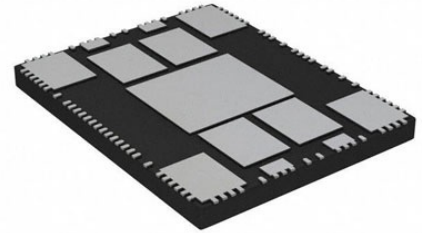


9FGL0841B 8 O/P PCIE GEN1-2-3 CLK GEN ZO=100OHMS; HT SUSA  
 CODE:8542390000

Manufacturers	<a href="#">Renesas Technology Corp</a>
Package/Case	VFQFPN-48
Product Type	Clock & Timer ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 9FGL0841BKILF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The 9FGL0841/51 devices are 8-output 3.3V PCIe Gen1–5 clock generators. Each output has a dedicated OE# pin supporting PCIe CLKREQ# functionality. Two different spread spectrum levels, in addition to spread off, are supported. The 9FGL0841/51 supports PCIe Gen1–5 Common Clocked architectures (CC), PCIe Separate Reference no-Spread (SRNS) and Separate Reference Independent Spread (SRIS) clocking architectures.

For information regarding evaluation boards and material, please contact your local sales representative.

## Features

PCIe Gen1–5 CC-compliant

Supports PCIe SRIS and SRNS clocking

Integrated terminations for 100Ω and 85Ω systems save 4 resistors per output

Pin-selectable SRnS 0%, CC 0% and CC/SRIS -0.5% spread

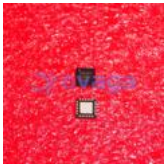
SMBus-selectable CC/SRIS -0.25% spread

One 3.3V LVCMOS REF output with Wake-On-LAN (WOL) support

Easy AC-coupling to other logic families, see application note AN-891.

Space saving 6 × 6 mm 48-VFQFPN

## Related Products



### [9FGV0241AKLFT](#)

Renesas Technology Corp  
VFQFPN-24



### [9FGV0241AKLIF](#)

Renesas Technology Corp  
24-VFQFN



### [49FCT3805QG](#)

Renesas Technology Corp  
QSOP-20



### [9FGV0841AKLFT](#)

Renesas Technology Corp  
48-VFQFN



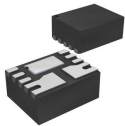
### [49FCT3805APYGI](#)

Renesas Technology Corp  
20-SSOP (0.209", 5.30mm Width)



### [49FCT3805ASOGI](#)

Renesas Technology Corp  
20-SOIC (0.295", 7.50mm Width)



### [9FGV0841AKLF](#)

Renesas Technology Corp  
MLF-48



### [9FGV0841AKLIF](#)

Renesas Technology Corp  
QFN-48