Dual, Low Noise, Wideband Variable Gain Amplifier, 0 dB To +40 dB Gain; Package: SOIC

- Wide; No of Pins: 16; Temperature Range: Industrial

| Manufacturers | Analog Devices, Inc |
| :--- | :--- |
| Package/Case | SOP-16 |
| Product Type | Amplifier ICs |
| RoHS | Rohs |



RoHS
Rohs

Images are for reference only
Lifecycle

Please submit RFQ for AD600ARZ or Email to us: sales@ovaga.com We will contact you in 12 hours.
RFQ

## General Description

The AD600/AD602 dual channel, low noise, variable gain amplifiers are optimized for use in ultrasound imaging systems, but are applicable to any application requiring precise gain, low noise and distortion, and wide bandwidth. Each independent channel provides a gain of 0 dB to +40 dB in the AD 600 and -10 dB to +30 dB in the AD 602 . The lower gain of the AD 602 results in an improved signal-to-noise ratio (SNR) at the output. However, both products have the same $1.4 \mathrm{nV} / \sqrt{ } \mathrm{Hz}$ input noise spectral density. The decibel gain is directly proportional to the control voltage, accurately calibrated, and supply and temperature-stable.

To achieve the difficult performance objectives, a proprietary circuit form, the X-AMP ${ }^{\circledR}$, was developed. Each channel of the X-AMP comprises a variable attenuator of 0 dB to -42.14 dB followed by a high speed fixed gain amplifier. In this way, the amplifier never has to cope with large inputs, and can benefit from the use of negative feedback to precisely define the gain and dynamics. The attenuator is realized as a 7-stage R-2R ladder network having an input resistance of 100 W , laser trimmed to $\pm 2 \%$. The attenuation between tap points is 6.02 dB ; the gain-control circuit provides continuous interpolation between these taps. The resulting control function is linear in dB .

The gain-control interfaces are fully differential, providing an input resistance of $\sim 15 \mathrm{M} \mathrm{W}$ and a scale factor of $32 \mathrm{~dB} / \mathrm{V}$ (that is, $31.25 \mathrm{mV} / \mathrm{dB}$ ) defined by an internal voltage reference. The response time of this interface is less than $1 \mu \mathrm{~s}$. Each channel also has an independent gating facility that optionally blocks signal transmission and sets the dc output level to within a few millivolts of the output ground. The gating control input is TTLand CMOS-compatible.

The maximum gain of the AD 600 is 41.07 dB , and the maximum gain of the AD 602 is 31.07 dB ; the -3 dB bandwidth of both models is nominally 35 MHz , essentially independent of the gain. The SNR for a 1 V rms output and a 1 MHz noise bandwidth is typically 76 dB for the AD600 and 86 dB for the AD602. The amplitude response is flat within $\pm 0.5 \mathrm{~dB}$ from 100 kHz to 10 MHz ; over this frequency range, the group delay varies by less than $\pm 2 \mathrm{~ns}$ at all gain settings.

Each amplifier channel can drive 100 W load impedances with low distortion. For example, the peak specified output is $\pm 2.5 \mathrm{~V}$ minimum into a 500 W load, or $\pm 1 \mathrm{~V}$ into a 100 W load. For a 200 W load in shunt with 5 pF , the total harmonic distortion for a $\pm 1 \mathrm{~V}$ sinusoidal output at 10 MHz is typically -60 dBc .

The AD600J/AD602J are specified for operation from $0^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ and are available in 16-lead PDIP (N) and 16-lead SOIC packages. The AD600A/AD602A are specified for operation from $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ and are available in 16-lead CERDIP (Q) and 16-lead SOIC packages. The $\mathrm{AD} 600 \mathrm{~S} / \mathrm{AD} 602 \mathrm{~S}$ are specified for operation from $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$, are available in a 16-lead CERDIP (Q) package, and are MIL-STD-883


AD600 - Gain Range: 0 dB to 40 dB
AD602 - Gain Range:-10 dB to +30 dB

## Features

2 channels with independent gain control Linear in dB gain response

2 gain ranges AD600: 0 dB to $40 \mathrm{dBAD} 602:-10 \mathrm{~dB}$ to +30 dB

Accurate absolute gain: $\pm 0.3 \mathrm{~dB}$

Low input noise: $1.4 \mathrm{nV} / \sqrt{ } \mathrm{Hz}$

Low distortion: - 60 dBc THD at $\pm 1 \mathrm{~V}$ output

High bandwidth: dc to $35 \mathrm{MHz}(-3 \mathrm{~dB})$

Stable group delay: $\pm 2 \mathrm{~ns}$

Low power: 125 mW (maximum) per amplifier
Signal gating function for each amplifier

Drives high speed ADCs

MIL-STD-883-compliant and DESC versions available

## Related Products



AD8418BRMZ-RL
Analog Devices, Inc
MSOP-8


ADA4084-2ARMZ
Analog Devices, Inc
MSOP-8


AD8567ARUZ
Analog Devices, Inc
TSSOP-14


AD8022ARMZ
Analog Devices, Inc
MSOP-8


## ADA4528-2ARMZ-R7

Analog Devices, Inc
MSOP-8

AD8062ARMZ
Analog Devices, Inc
MSOP8

AD8628AUJZ
Analog Devices, Inc
SOP23

AD8041AR
Analog Devices, Inc
SOP-8

