

FPGA Spartan®-II Family 100K Gates 2700 Cells 263MHz 0.18um Technology 2.5V

Manufacturers	AMD Xilinx, Inc
Package/Case	208-BFQFP
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S100-6PQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC2S100-6PQ208C is a type of field-programmable gate array (FPGA) manufactured by Xilinx. Here are some details about this specific FPGA:

Features

- "XC2S100" refers to the specific model of the Xilinx Spartan-II family.
- "-6" indicates that the FPGA has a maximum operating frequency of 6 MHz.
- "PQ208C" describes the package type, which is a plastic quad flat package with 208 pins.

Application

- 100,000 system gates (equivalent to roughly 2,000 logic cells)
- 144 macrocells (configurable logic blocks)
- 4 global clock lines and 8 additional clock lines
- 3600 flip-flops
- 3328 4-input LUTs
- 168 Kb of dual-port RAM
- 100 MHz operation



Related Products



[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



[XCV50-6BG256C](#)

AMD Xilinx, Inc
BGA256



[XCF08PV0G48C](#)

AMD Xilinx, Inc
TSOP-48



[XC6SLX25-3FTG256C](#)

AMD Xilinx, Inc
BGA-256



[XC6SLX16-3CSG324C](#)

AMD Xilinx, Inc
BGA-324



[XCF32PVO48C](#)

AMD Xilinx, Inc
TSOP48