

FDG6322C

Data Sheet

Dual N & P Channel Digital FET, MOSFET SC70-6 COMP N-P-CH

Manufacturers	ON Semiconductor, LLC	
Package/Case	SC-70-6	
Product Type	Transistors	
RoHS	Green	
Lifecycle		Images are for reference only
Please submit RFQ for FDG6322C or <u>Email to us: sales@ovaga.com</u> We will contact you in 12 hours.		

General Description

These dual N & P-Channel logic level enhancement mode field effect transistors are produced using a proprietary, high cell density, DMOS technology. This very high density process is especially tailored to minimize on-state resistance. This device has been designed especially for low voltage applications as a replacement for bipolar digital transistors and small signal MOSFETs. Since bias resistors are not required, this dual digital FET can replace several different digital transistors, with different bias resistor values.

Features

N-Ch 0.22 A, $25 = 5.0 \Omega$ @>

P-Ch-0.41 = 1.5 Ω @>

Very small package outline SC70-6.

Very low level gate drive requirements allowing direct operation in 3 V circuits (V GS(th) < 1.5 V)

Gate-Source Zener for ESD ruggedness (>6k V Human Body Model).

Related Products



ON Semiconductor, LLC SSOT-6

FDC645N



FDC637BNZ

ON Semiconductor, LLC SSOT-6

Application

ONSEMI



FDC6305N

ON Semiconductor, LLC SSOT-6



FDD8647L ON Semiconductor, LLC TO-252-3



FDD5N50NZTM ON Semiconductor, LLC

TO-252



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FDC602P

ON Semiconductor, LLC SOT163

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ON Semiconductor, LLC TO-263

FDD3670

ON Semiconductor, LLC TO-252AA