

Low Skew 1 to 4 Clock Buffer

Manufacturers

[Renesas Technology Corp](#)

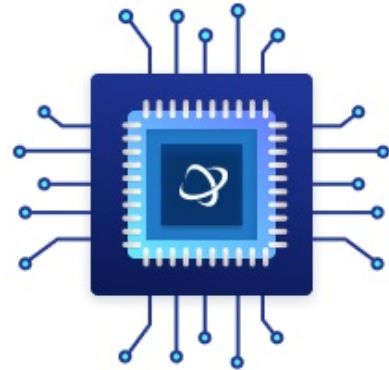
Package/Case

Product Type

Clock & Timer ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for 524SDCGI or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

The 524S is a low skew, single input to four output, LVCMOS clock buffer. The 524S has best in class additive phase Jitter of sub 50 fsec.

Features

Low additive phase jitter RMS: 50fs

Extremely low skew outputs (50ps)

Low cost clock buffer

Packaged in 8-SOIC and 8-DFN, Pb-free

ICLK is PDT and may be driven before VDD is applied

Direct-coupled signal path suitable for 1pps clocks

Input / Output clock frequency up to 200MHz

Non-inverting output clock

Ideal for networking clocks

Operating voltages: 1.8V to 3.3V

Advanced, low power CMOS process

Extended temperature range (-40°C to +105°C)

Related Products



[524MILFT](#)

Renesas Technology Corp
SOP8



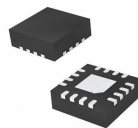
[524SCMGI](#)

Renesas Technology Corp
8-UFDN



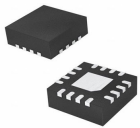
[552G-02ILN](#)

Renesas Technology Corp
TSSOP-16



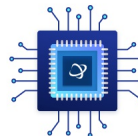
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Renesas Technology Corp
VFQFN-32



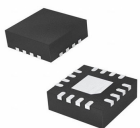
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Renesas Technology Corp
VFQFN-32



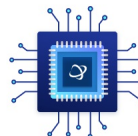
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32-VFQFN



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