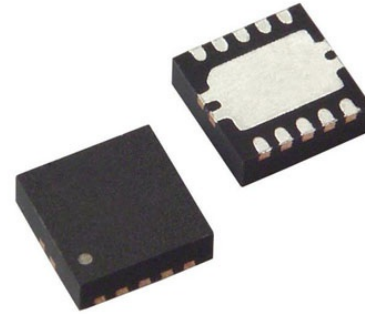


Single PHY/MII/LinkMD/Automotive

Manufacturers	Microchip Technology, Inc
Package/Case	VQFN
Product Type	Interface ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for KSZ8061MNXV or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The KSZ8061 is a family of single-chip 10Base-T/100Base-TX Ethernet physical layer transceivers for transmission and reception of data over unshielded twisted pair (UTP) cable. The KSZ8061 features Quiet-WIRE® internal filtering to reduce line emissions. It is ideal for applications, such as automotive or industrial networks, where stringent radiated emission limits need to be met. Quiet-WIRE can use low-cost unshielded cable, where previously only shielded cable solutions were possible. The KSZ8061 also features enhanced immunity to environmental EM noise. The KSZ8061MNX offers the Media Independent Interface (MII) for direct connection with MII-compliant Ethernet MAC processors and switches. The KSZ8061RNx features a Reduced Media Independent Interface (RMII) for direct connection with RMII-compliant Ethernet MAC processors and switches. The KSZ8061RNB generates a 50MHz RMII reference clock for use by the connected MAC device. In contrast, the KSZ8061RND receives the 50MHz RMII reference clock as an input. They are designed to exceed Automotive AEC-Q100 and EMC requirements, and feature an extended temperature range of -40°C to +105°C. The KSZ8061MNX is supplied in a 32-lead, 5mm x 5mm QFN or WQFN package. The KSZ8061MNG is in a 48-lead, 7mm x 7mm QFN package. The KSZ8061RNx are supplied in 32-pin, 5mm x 5mm QFN and WQFN packages. Microchip's complimentary and confidential LANCheck® online design review service is available for customers who have selected our products for their application design-in. The LANCheck online design review service is subject to Microchip's Program Terms and Conditions and requires a myMicrochip account.

Features

Quiet-WIRE® programmable EMI filter

MII/RMII interface with MDC/MDIO management interface for register configuration

On-chip termination resistors for the differential pairs

LinkMD®+ receive signal quality indicator

Fast start-up and link

Ultra-Deep Sleep standby mode: CPU or signal detect activated

Loopback modes for diagnostics

Programmable interrupt output

Related Products



[KSZ8081MLXIA](#)

Microchip Technology, Inc
LQFP-48



[KSZ8721BLLI-TR](#)

Microchip Technology, Inc
LQFP-48



[KSZ8041NLLI-TR](#)

Microchip Technology, Inc
VQFN-32



[KSZ8091RNBCA](#)

Microchip Technology, Inc
VQFN-32



[KSZ8721BT](#)

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TQFP-48



[KSZ8091RNAIA-TR](#)

Microchip Technology, Inc
VQFN-24



[KSZ8721B](#)

Microchip Technology, Inc
SSOP-48



[KSZ8061MNGW](#)

Microchip Technology, Inc
VQFN-48