

FPGA Spartan®-3 Family 50K Gates 1728 Cells 630MHz 90nm Technology 1.2V

Manufacturers	<a href="#">AMD Xilinx, Inc</a>
Package/Case	QFP208
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC3S50-4PQ208I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

XC3S50-4PQ208I is a product code for a Field-Programmable Gate Array (FPGA) developed by Xilinx, a leading manufacturer of programmable logic devices. Here are some key features and specifications of this FPGA:

### Features

Family: Spartan-3

Number of logic cells: 3,456

Number of available I/O pins: 101

Operating temperature range: -40°C to +100°C

Speed grade: -4 (maximum operating frequency of 400 MHz)

Package type: 208-pin plastic quad flat pack (PQFP)

### Application

Digital signal processing

Communications systems

Industrial control systems

Video processing and display

Automotive applications

Aerospace and defense systems

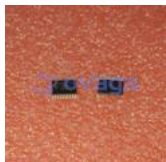


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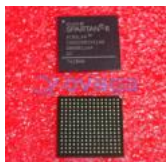
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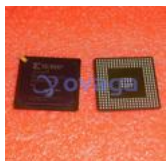
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