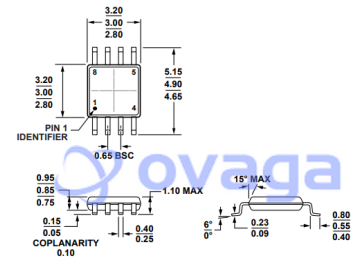


3 MSPS, 12-Bit ADC in 6-Lead TSOT; Package: MSOP; No of Pins: 8; Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	MSOP-8
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



COMPLIANT TO JEDEC STANDARDS MO-187-AA
Figure 38. 8-Lead Mini Small Outline Package (MSOP) (RM-8)
Dimensions shown in millimeters

Images are for reference only

Please submit RFQ for AD7276BRMZ-REEL or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The conversion process and data acquisition are controlled using CS and the serial clock, allowing the devices to interface with microprocessors or DSPs. The input signal is sampled on the falling edge of CS, and the conversion is also initiated at this point. There are no pipeline delays associated with the part.

The AD7276 uses advanced design techniques to achieve very low power dissipation at high throughput rates.

The reference for the part is taken internally from VDD. This allows the widest dynamic input range to the ADC; therefore, the analog input range for the part is 0 to VDD. The conversion rate is determined by the SCLK.

Product Highlights

3 MSPS ADCs in a 6-lead TSOT package

AD7476/AD7477/AD7478 and AD7476A/AD7477A/ AD7478A pin-compatible

High throughput with low power consumption

Flexible power/serial clock speed management. This allows maximum power efficiency at low throughput rates

Reference derived from the power supply

No pipeline delay. The parts feature a standard successive approximation ADC with accurate control of the sampling instant via a CS input and once-off conversion control.

Features

Throughput rate: 3 MSPS

Specified for VDD of 2.35 V to 3.6 V

Power consumption 12.6 mW at 3 MSPS with 3 V supplies

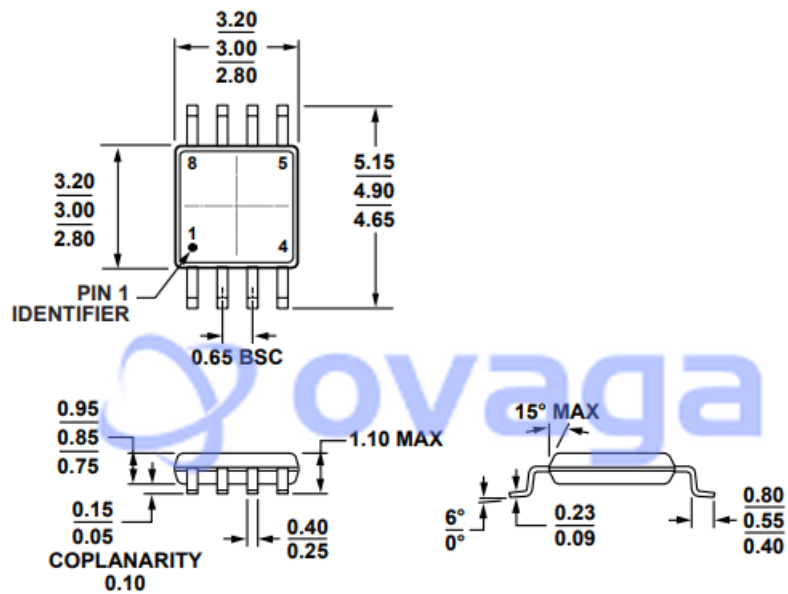
Wide input bandwidth 70 dB SNR at 1 MHz input frequency

Flexible power/serial clock speed management

No pipeline delays

Temperature range: -40°C to $+125^{\circ}\text{C}$

Please see data sheet for additional features.



COMPLIANT TO JEDEC STANDARDS MO-187-AA

Figure 38. 8-Lead Mini Small Outline Package [MSOP]
(RM-8)

Dimensions shown in millimeters

100709-B

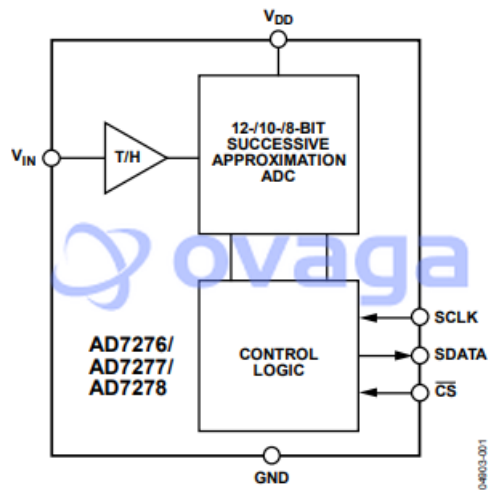
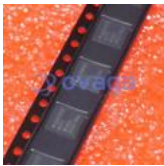


Figure 1.

Related Products



[ADAS3022BCPZ](#)

Analog Devices, Inc
LFCSP-40



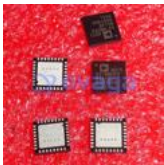
[AD574AJNZ](#)

Analog Devices, Inc
PDIP-28



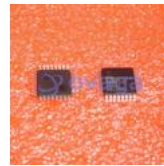
[AD7938BSUZ](#)

Analog Devices, Inc
TQFP-32



[AD7124-8BCPZ-RL7](#)

Analog Devices, Inc
LFCSP-32



[AD7266BSUZ](#)

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[AD7192BRUZ-REEL](#)

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TSSOP-24



[AD9680BCPZ-500](#)

Analog Devices, Inc
LFCSP-64