

256 MACROCELL 1.8V ZERO POWER ISP CPLD

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP-100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2C256-7VQG100I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC2C256-7VQG100I is a field-programmable gate array (FPGA) device from Xilinx, which belongs to the CoolRunner-II family.

Features

- Logic cells: 2560
- Maximum number of user I/Os: 154
- Block RAM: 32 Kbits
- Clock management tiles: 4
- Maximum user-accessible global clock frequency: 275 MHz
- Operating voltage: 1.8V - 3.3V
- Operating temperature range: -40°C to +100°C

Application

- Communications: Ethernet switches, routers, network interface cards, etc.
- Consumer electronics: Set-top boxes, DVD players, digital cameras, etc.
- Industrial automation: Motor control, robotics, sensors, etc.
- Automotive: Infotainment systems, driver assistance systems, etc.
- Aerospace and defense: Radar systems, avionics, etc.

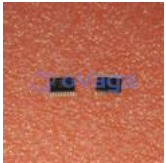


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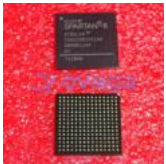
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



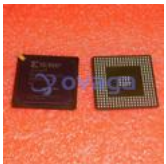
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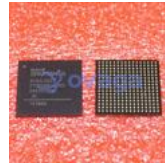
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[XC6SLX25-3FTG256C](#)

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