

Noninverting Buffer / CMOS Logic Level Shifter with LSTTL-Compatible Inputs, Buffers & Line Drivers 5V Level Shift Non-Inverting LSTTL

Manufacturers	ON Semiconductor, LLC
Package/Case	SOIC-14
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for MC74VHCT50ADR2G or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The MC74VHCT50A is a hex noninverting buffer fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation. The internal circuit is composed of three stages, including a buffered output which provides high noise immunity and stable output. The device input is compatible with TTL-type input thresholds and the output has a full 5 V CMOS level output swing. The input protection circuitry on this device allows overvoltage tolerance on the input, allowing the device to be used as a logic-level translator from 3.0 V CMOS Logic while operating at the high-voltage power supply. The MC74VHCT50A input structure provides protection when voltages up to 7.0 V are applied, regardless of the supply voltage. This allows the MC74VHCT50A to be used to interface 5 V circuits to 3 V circuits. The output structures also provide protection when etc.

Features

High Speed: = 5V

Low Power Dissipation: = 25°C

TTL-Compatible Inputs: = 2.0V

CMOS-Compatible Outputs: $V_{OH} > 0.8V_{CC}$; $V_{OL} < 0.1V_{CC}$ @Load

Power Down Protection Provided on Inputs and Outputs

These devices are available in Pb-free package(s). Specifications herein apply to both standard and Pb-free devices. Please see our website at www.onsemi.com for specific Pb-free orderable part numbers, or contact your local ON Semiconductor sales office or representative.

Application

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SOIC-16



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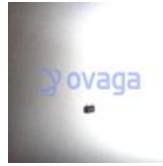
[MC14013BDR2G](#)

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