EPM7256AETC100-10N

Data Sheet

CPLD MAX® 7000A Family 5K Gates 256 Macro Cells 95.2MHz 3.3V 100-Pin TQFP Tray

Images are for reference only

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	

Please submit RFQ for EPM7256AETC100-10N or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

EPM7256AETC100-10N is a type of Field-Programmable Gate Array (FPGA) developed by Altera (now part of Intel Corporation).

Features

It has a capacity of 256 macrocells (equivalent to logic elements)

Operating speed of 10 nanoseconds

5V power supply

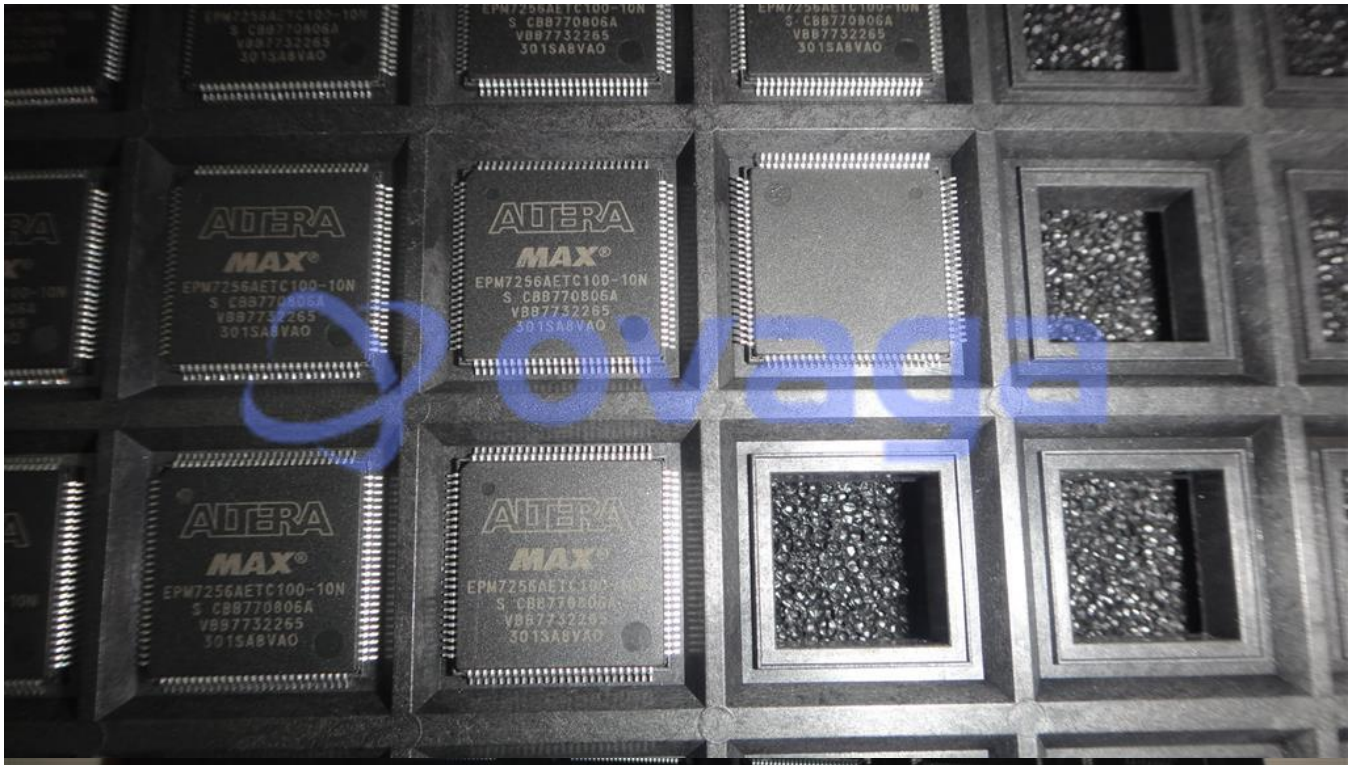
Comes in a 100-pin TQFP (Thin Quad Flat Package) package

Can be reprogrammed to implement different logic functions

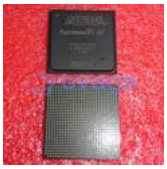
Application

It can be used in a variety of digital circuit applications where reprogrammable logic is required, such as in networking devices, test and measurement equipment, and industrial automation.

Specifically, EPM7256AETC100-10N can be used for implementing complex state machines, digital signal processing, and control logic.



Related Products



EP4CE55F29C8N

Altera Corporation (Intel)

FBGA-780

EPM240M100C5N

Altera Corporation (Intel)

BGA-100

EPM1270T144A5N

Altera Corporation (Intel)

TQFP-144

EPM570F256C5N

Altera Corporation (Intel)

FBGA-256

EP2C35F672C8N

Altera Corporation (Intel)

FBGA-672

EPM7128AETC100-10

Altera Corporation (Intel)

TQFP-100

EP2C35F484C7N

Altera Corporation (Intel)

FBGA-484

EP2C35F484I8N

Altera Corporation (Intel)

FBGA-484