

IC CLOCK BUFFER; HT SUSAS CODE:8542390000

Manufacturers	Renesas Technology Corp
Package/Case	20-TSSOP (0.173, 4.40mm Width)
Product Type	Clock & Timer ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 853S014AGILFT or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 853S014I is a low skew, high performance 1-to-5, 2.5V/3.3V Differential-to-LVPECL/ECL Fanout Buffer. The 853S014I has two selectable clock inputs. Guaranteed output and part-to-part skew characteristics make the 853S014I ideal for those applications demanding well defined performance and repeatability.

Features

Five differential LVPECL/ECL outputs

Two selectable differential LVPECL clock inputs

PCLKx, nPCLKx pairs can accept the following differential input levels: LVPECL, LVDS, CML, SSTL

Maximum output frequency: 2GHz

Output skew: 55ps (maximum)

Part-to-part skew: 100ps (maximum)

Propagation delay: 500ps (maximum)

Additive phase jitter, RMS: 0.10ps (maximum)

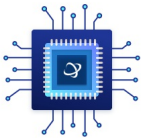
LVPECL mode operating voltage supply range: = 0V

ECL mode operating voltage supply range: = -3.8V to -2.375V

-40°C to 85°C ambient operating temperature

Available in lead-free (RoHS 6) packages

Related Products



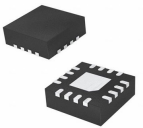
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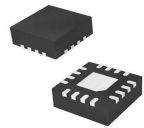
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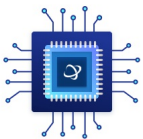
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Renesas Technology Corp
16-VFQFN



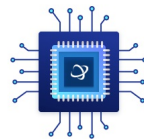
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Renesas Technology Corp
16-VFQFN



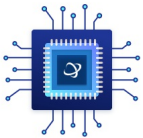
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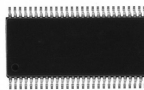
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8-SOIC (0.154, 3.90mm Width)