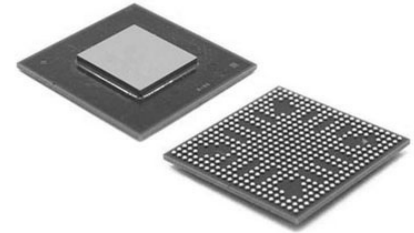


Conv DC-DC 4.5V to 60V Synchronous Step Down Single-Out 0.8V to 60V 0.05A 8-Pin LFCSP EP T/R

Manufacturers	Analog Devices, Inc
Package/Case	LFCSP-8
Product Type	Power Management ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADP2360ACPZ-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADP2360 is a high efficiency, high input voltage, discontinuous conduction mode (DCM) synchronous, step-down, dc-to-dc switching regulator. The ADP2360 operates with a wide input voltage range from 4.5 V to 60 V and can source up to 50 mA continuous output current, making it ideal for regulating power from a variety of voltage sources in space-constrained applications. The ADP2360 is available with an adjustable output (0.8 V to VIN) or in 3.3 V and 5.0 V factory-programmable fixed output voltage models.

The ADP2360 uses a single-pulse PFM architecture with adjustable IPEAK control to minimize the input and output ripple. The adjustable IPEAK current limit allows the ADP2360 to optimize the efficiency for the application operating conditions and minimize the compatible inductor size.

The ADP2360 further offers a power-good (PG) pin to indicate when the output voltage is good. Other key features include 100% duty cycle operation, precision enable control, external soft start control, undervoltage lockout, and thermal shutdown.

The ADP2360 requires no external compensation and the solution for the fixed output voltage options requires a minimum of three external components. The ADP2360 is available in a 3 mm × 3 mm, 8-lead LFCSP package with an operating junction temperature range from -40°C to +125°C.

Features

Input voltage range: 4.5 V to 60 V

Adjustable output voltage range: 0.8 V to VIN

Factory programmable fixed output voltage options of 3.3 V and 5.0 V

Continuous output current up to 50 mA

Adjustable peak current limit up to 140 mA

Pulse frequency modulation (PFM) control

Very high efficiency

90% at = 3.3 V,>

87% at = 3.3 V,>

Low shutdown current: <4 μ A

Low quiescent current: 12 μ A in sleep mode

100% duty cycle operation

Undervoltage lockout (UVLO)

No external compensation required

Enable input with precision thresholds

Programmable soft start

Power-good output

Thermal shutdown (TSD) protection

8-lead LFCSP package

Application

4 mA to 20 mA loop powered systems

HART modems

Building automation

Distributed power systems

Industrial control supplies

Other high VIN, low IOOUT systems

Related Products



[ADP3336ARMZ-REEL7](#)

Analog Devices, Inc
MSOP-8



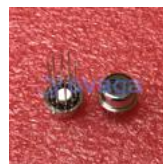
[AD737JRZ](#)

Analog Devices, Inc
SOP-8



[ADP3367ARZ](#)

Analog Devices, Inc
SOIC-8



[AD636JH](#)

Analog Devices, Inc
TO-100-10



[ADP3330ARTZ3.3-RL7](#)

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SOT-23-6